3A SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

FEATURES:

- · Glass Passivated Chip Junction
- Reverse Voltage 100 to 1000 V
- Forward Current 3.0 A
- Fast reverse recovery time
- Designed for Surface Mount Application

MECHANICAL DATA

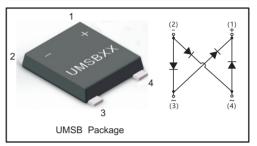
· Case: UMSB

• Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.234g / 0.00825oz

PINNING

PIN	DESCRIPTION		
1	Output Anode (+)		
2	Output Cathode (-)		
3	Input Pin(~)		
4	Input Pin (~)		



Maximum Ratings and Electrical characteristics

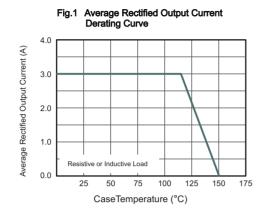
Ratings at 25 °C ambient temperature unless otherwise specified.

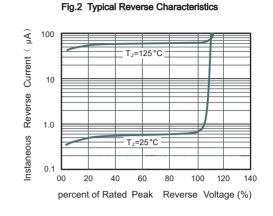
Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

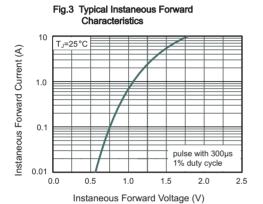
Parameter	Symbols	FMSB30B	FMSB30D	FMSB30G	FMSB30J	FMSB30K	FMSB30M	Units
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	100	200	400	600	800	1000	V
Maximum RMS voltage	V_{RMS}	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V _{DC}	100	200	400	600	800	1000	V
Average Rectified Output Current	Io	3.0				А		
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)	I _{FSM}	90				А		
Maximum Forward Voltage at 3.0 A	V _F	1.3				٧		
Maximum DC Reverse Current T _a = 25 °C at Rated DC Blocking Voltage T _a =125 °C	I _R	5.0 200				μА		
Typical Junction Capacitance (Note1)	C _j	40				pF		
Typical Thermal Resistance (Note2)	R _{eja} R _{ejc} R _{ejl}	32 15 13				°C/W		
Maximum Reverse Recovery Time (Note3)	t _{rr}	150 250 500		00	ns			
Operating and Storage Temperature Range	T_{j},T_{stg}	-55 ~ +150				°C		

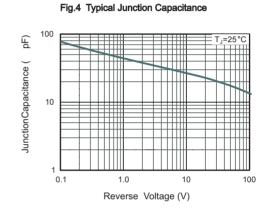
Note: 1. Measured at 1 MHz and applied reverse voltage of 4 V D.C

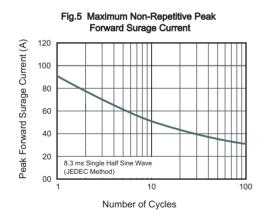
- 2. Mounted on glass epoxy PC board with 4 ×1.5" ×1.5" (3.81×3.81cm) copper pad.
- 3. Measured with $I_F = 0.5 \text{ A}$, $I_R = 1 \text{ A}$, $I_{rr} = 0.25 \text{ A}$.







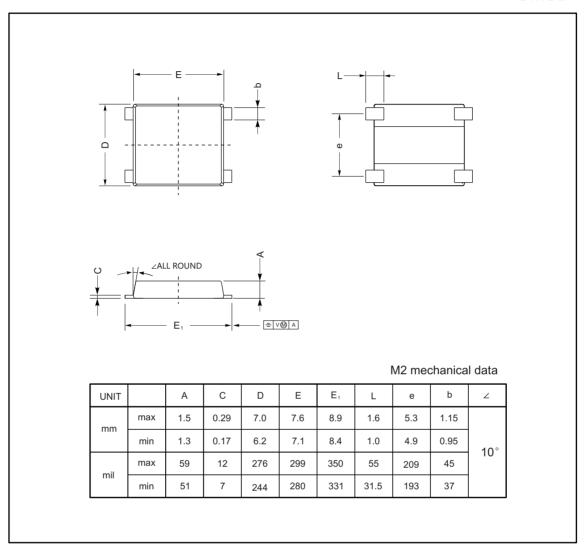




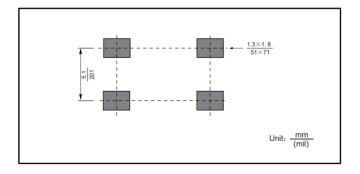
PACKAGE OUTLINE

Plastic surface mounted package; 4 leads

UMSB



The recommended mounting pad size



Marking

Type number	Marking code		
FMSB30B	FMB30B		
FMSB30D	FMB30D		
FMSB30G	FMB30G		
FMSB30J	FMB30J		
FMSB30K	FMB30K		
FMSB30M	FMB30M		

文件履历表

序号	制/修订日期	生效日期	版次	修订内容	变更原因	制/修订人	备注
01	2018. 2. 28	2018. 3. 2	Rev 1.1	初版制定	/	张阁	
02	2023. 2. 7	2023. 2. 9	Rev 1.2	添加84mi1芯片	市场需求	郭金铮	

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